FIG. 1

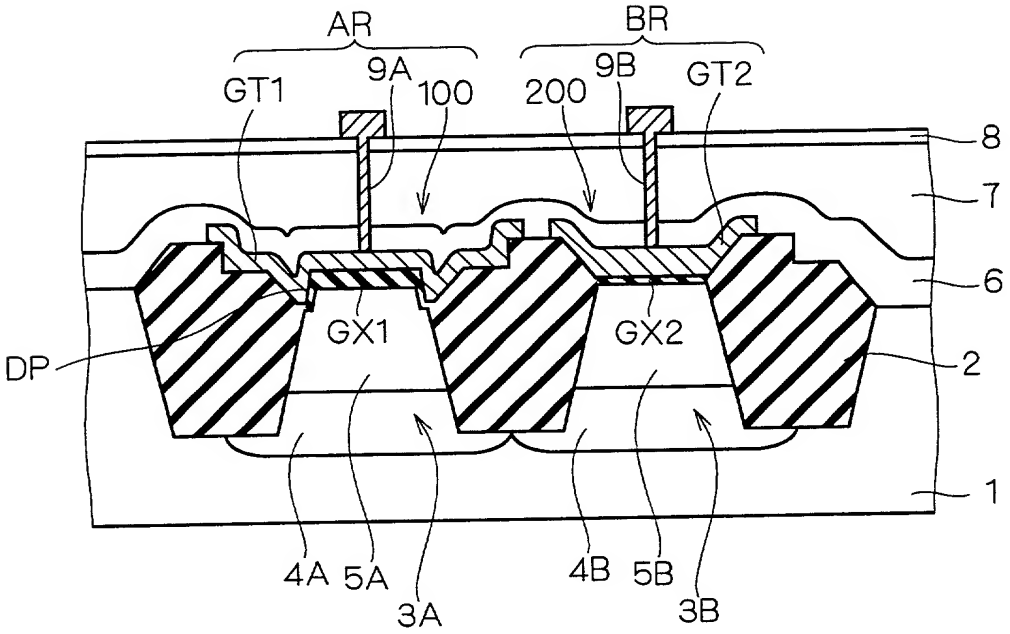
AR
BR
GT1
9A
100
200
9B
GT2
8
7
6
DP
GX1
GX2
2
1
4A
5A
3A
4B
5B
3B

FIG. 2

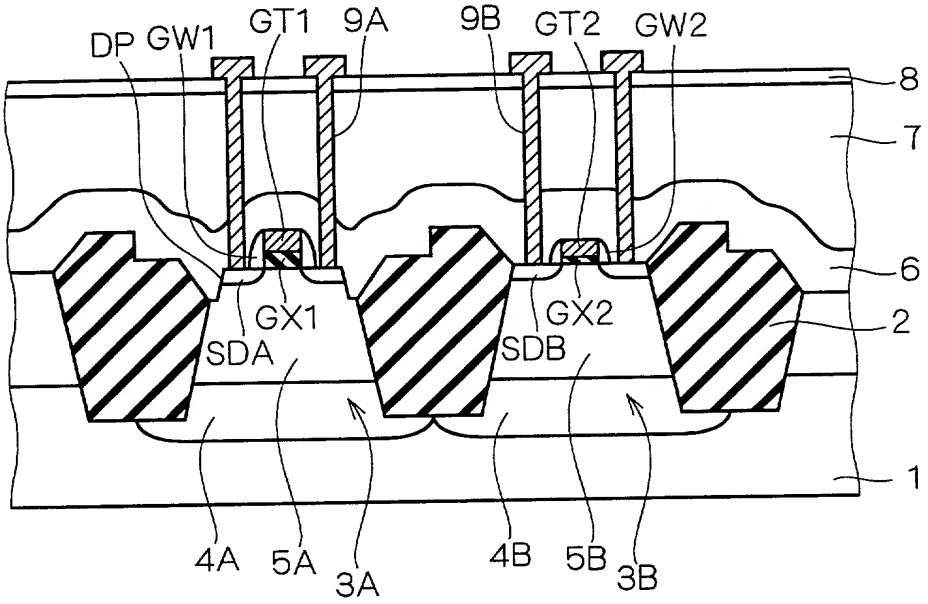
DP
GW1
GT1
9A
9B
GT2
GW2
8
7
6
GX1
GX2
SDA
SDB
2
1
4A
5A
3A
4B
5B
3B

FIG. 3

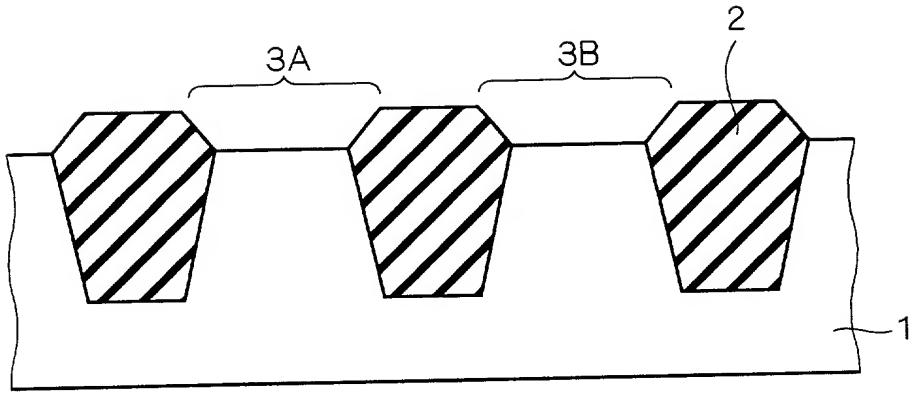

FIG. 4

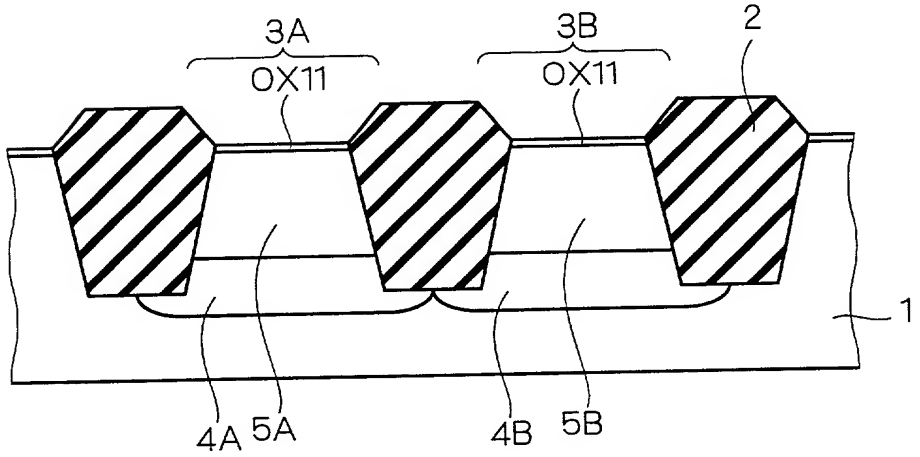

FIG. 5

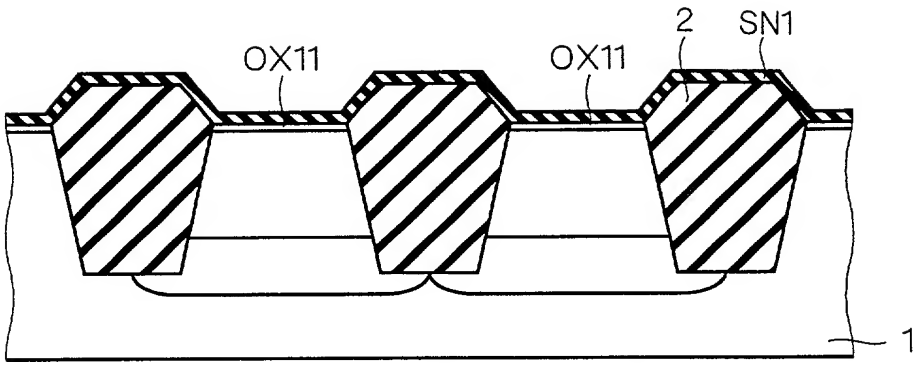

FIG. 6

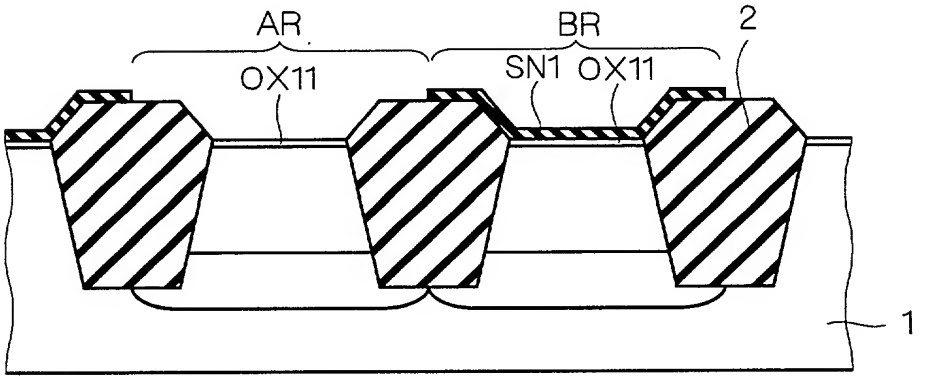

FIG. 7

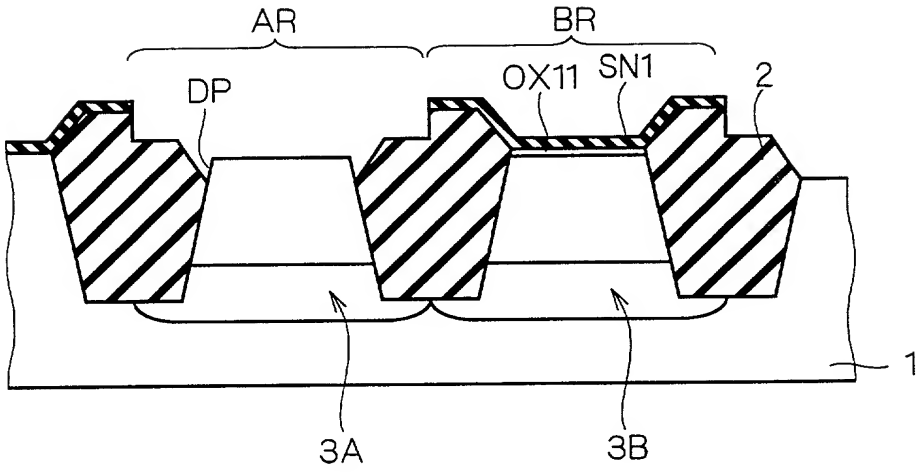

FIG. 8

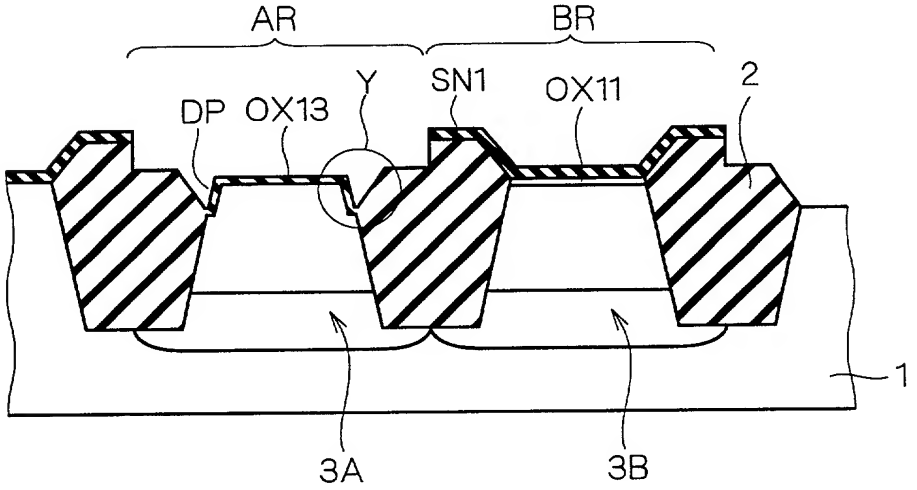

FIG. 9

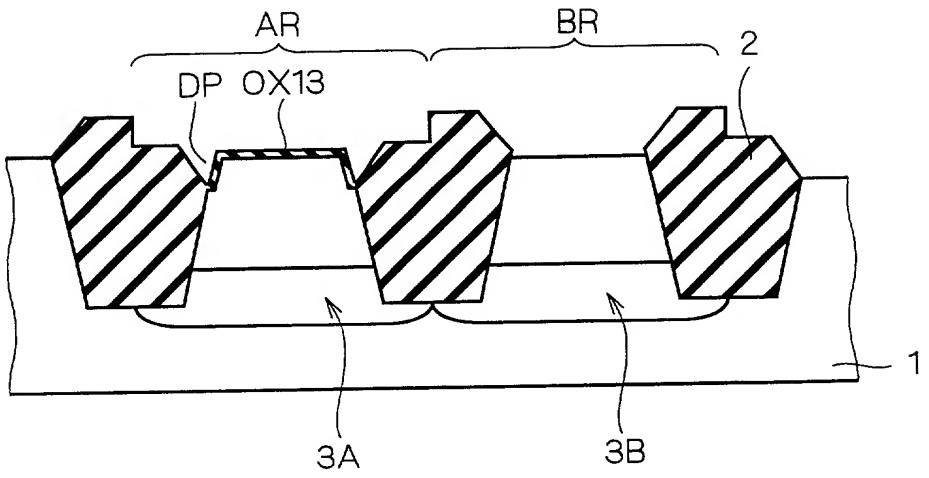

FIG. 10

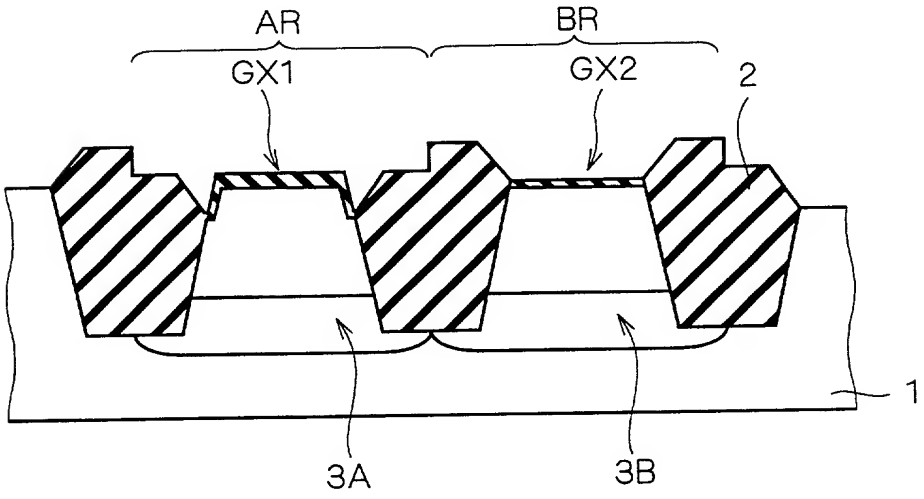

FIG. 11

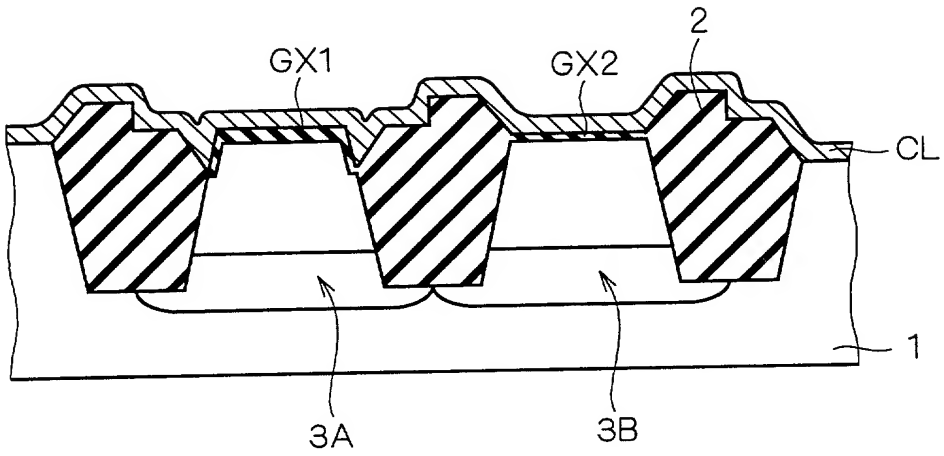

FIG. 12

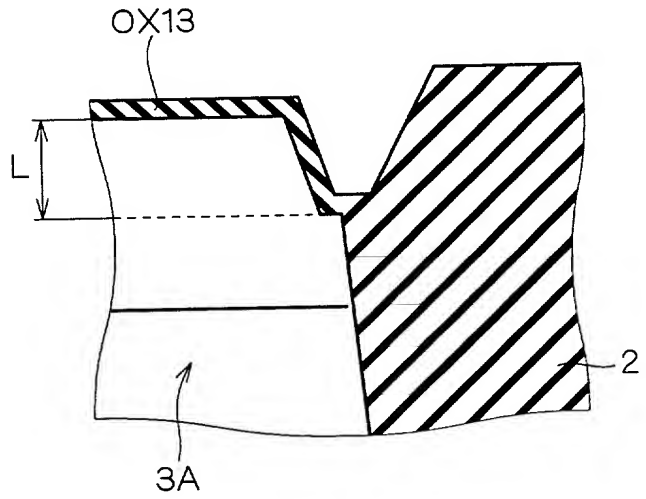

FIG. 13

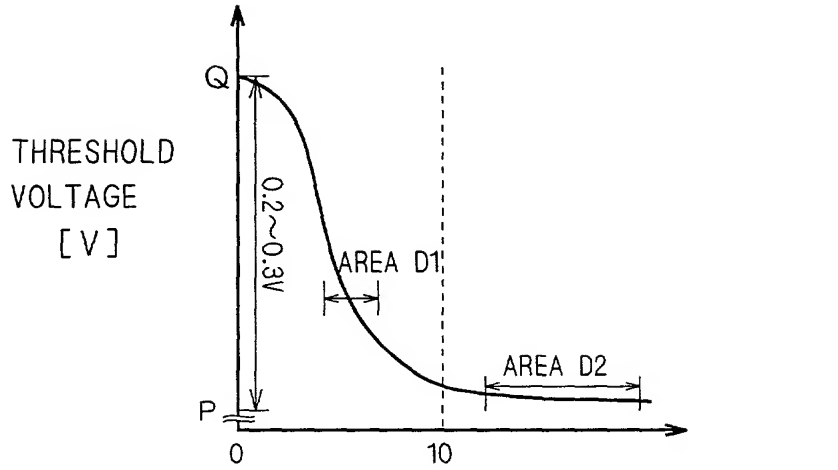
THRESHOLD VOLTAGE [V]
Q
P
0.2~0.3V
AREA D1
AREA D2
0
10
DEPTH OF RECESS AROUND ACTIVE REGION (nm)

FIG. 14

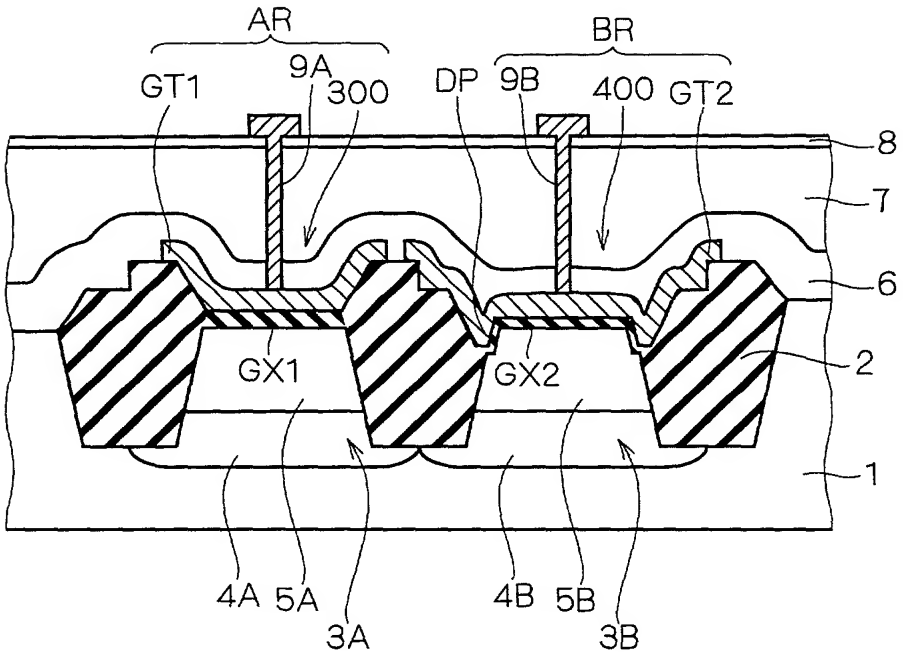
AR
BR
GT1
9A
300
DP
9B
400
GT2
8
7
6
2
1
GX1
GX2
4A
5A
3A
4B
5B
3B

FIG. 15

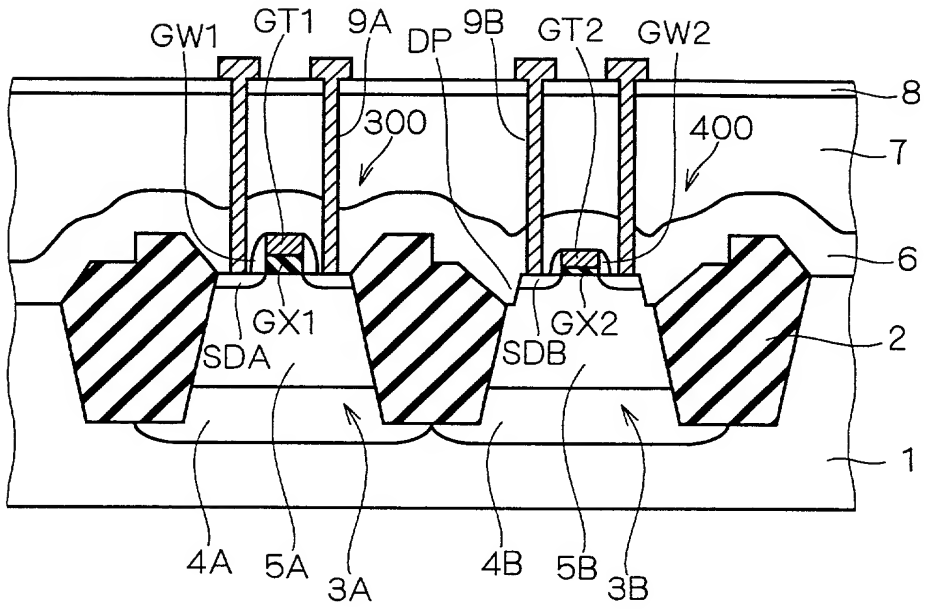
GW1
GT1
9A
DP
9B
GT2
GW2
8
300
400
7
6
GX1
SDA
SDB
GX2
2
1
4A
5A
3A
4B
5B
3B

FIG. 16

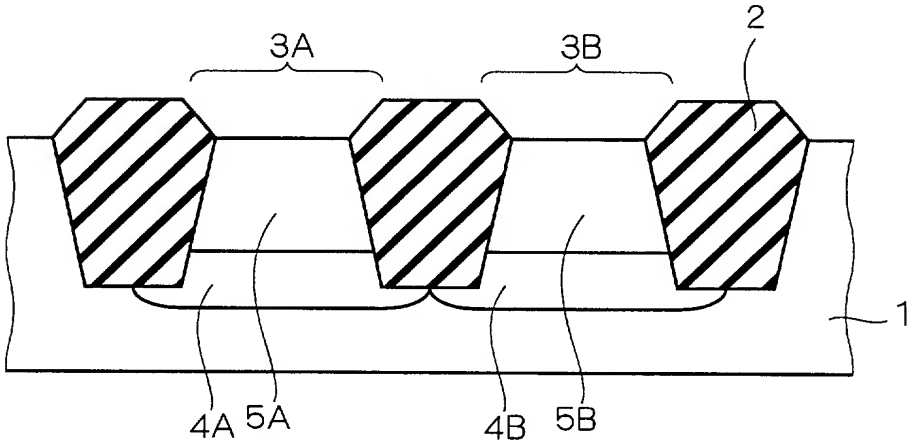
3A
3B
2
1
4A
5A
4B
5B

FIG. 17

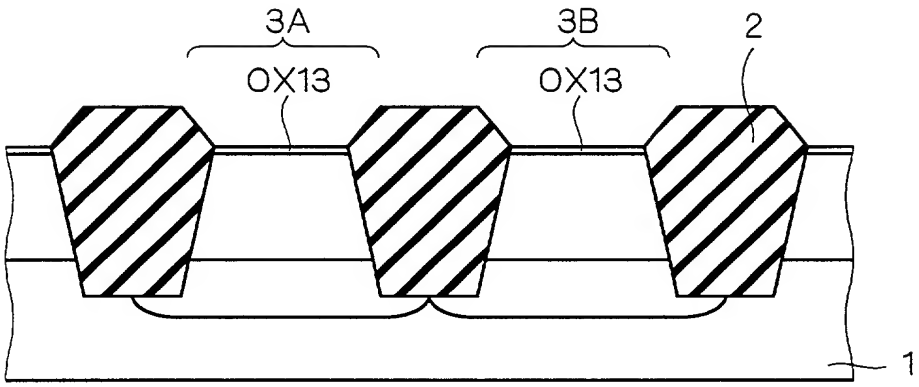

FIG. 18

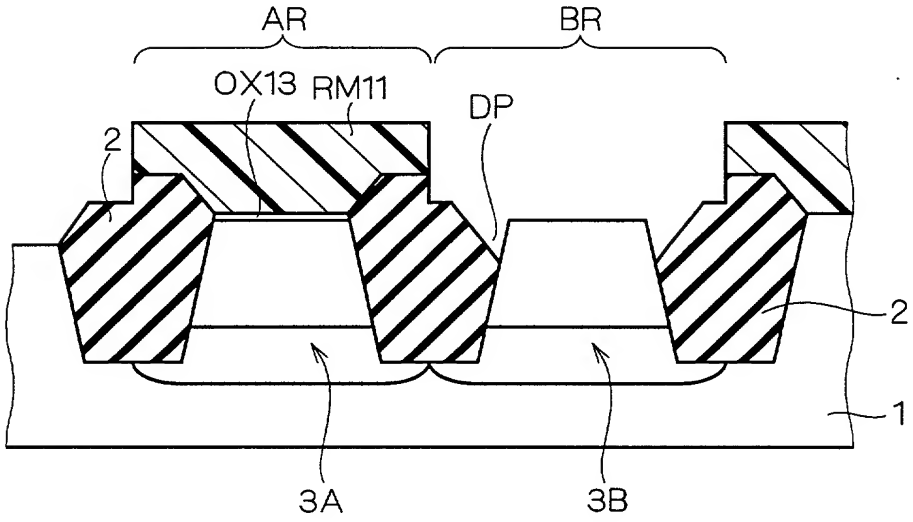

FIG. 19

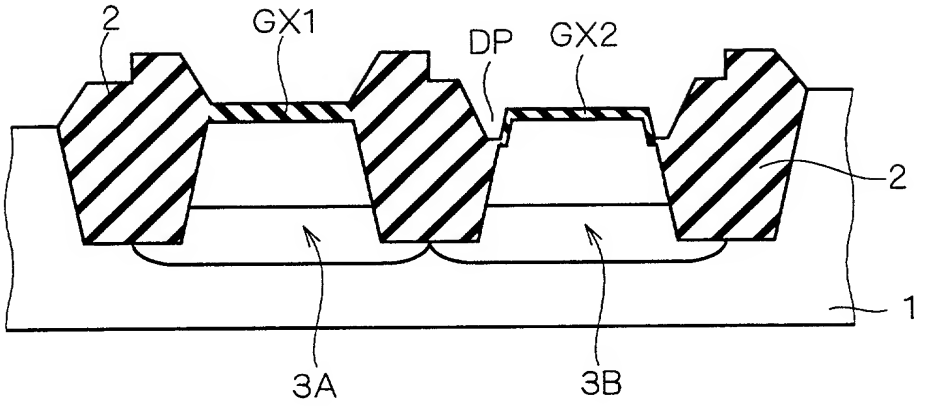

FIG. 20

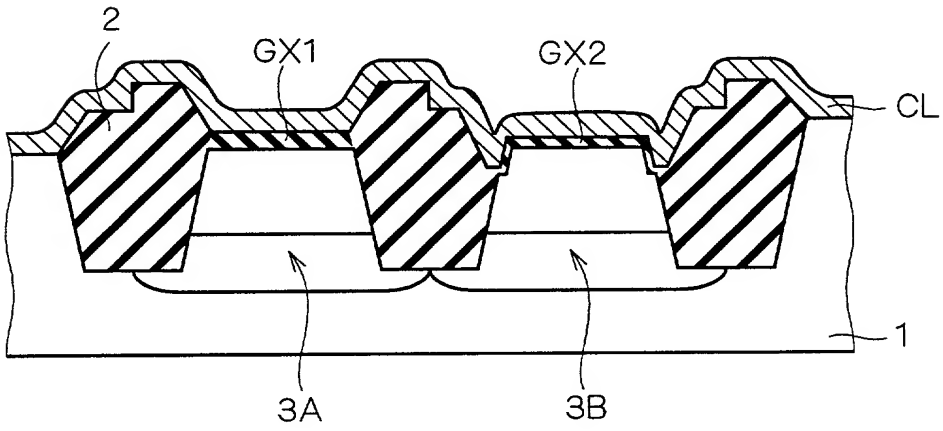

FIG. 21

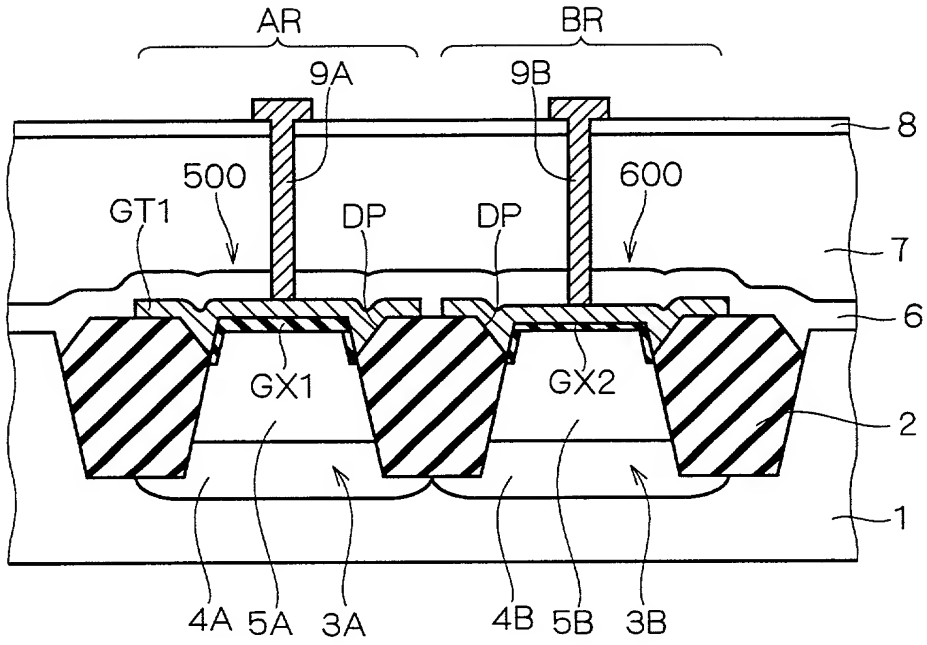
AR
BR
9A
9B
8
500
600
GT1
DP
DP
7
6
GX1
GX2
2
1
4A
5A
3A
4B
5B
3B

FIG. 22

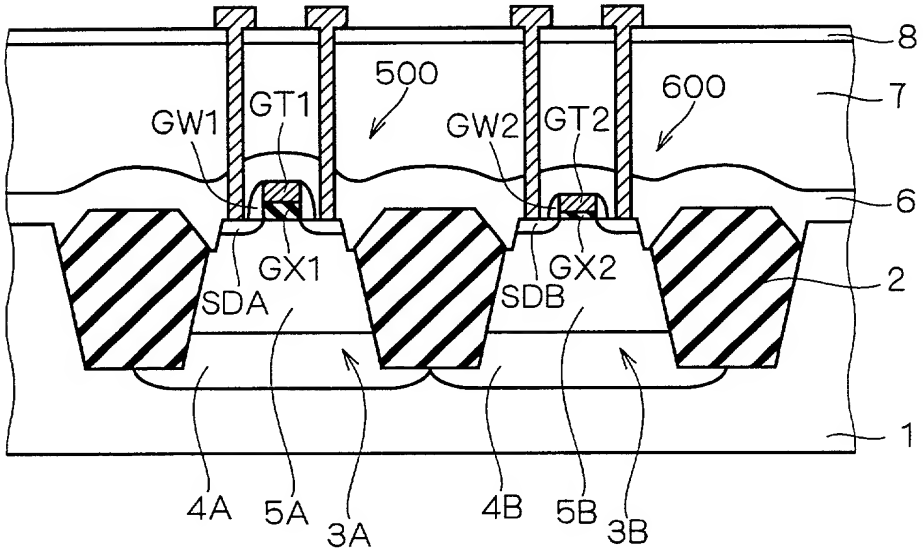
8
500
600
GW1
GT1
GW2
GT2
7
6
GX1
GX2
SDA
SDB
2
1
4A
5A
3A
4B
5B
3B

FIG. 23

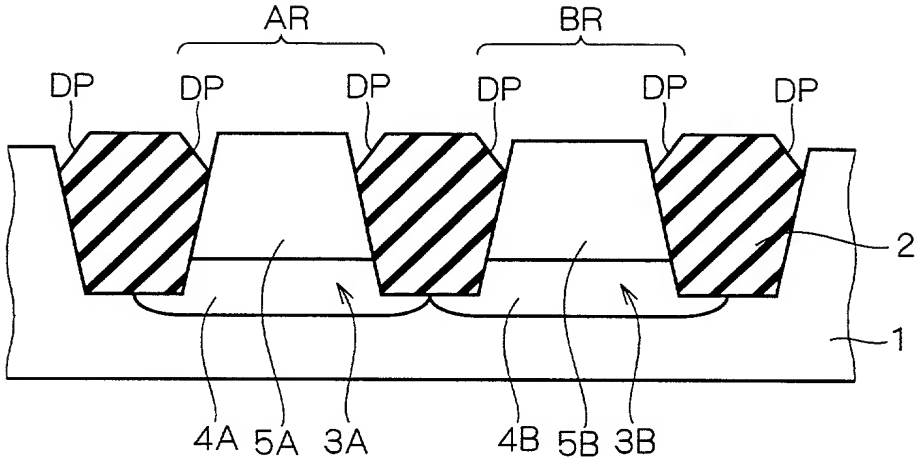

FIG. 24

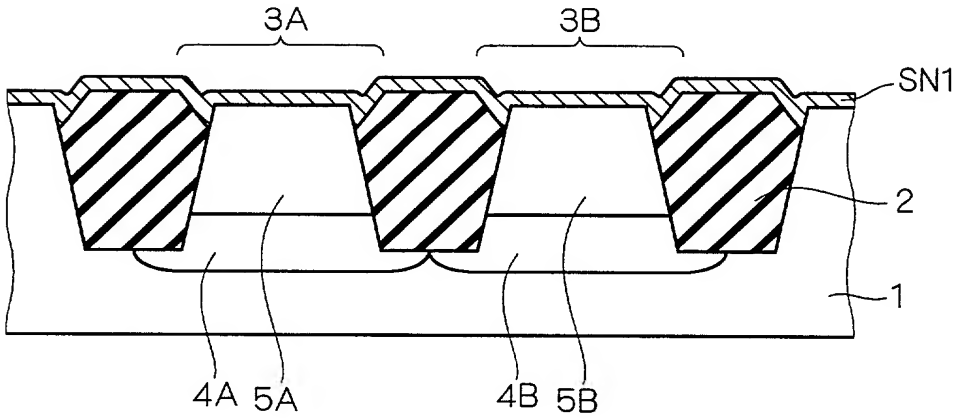

FIG. 25

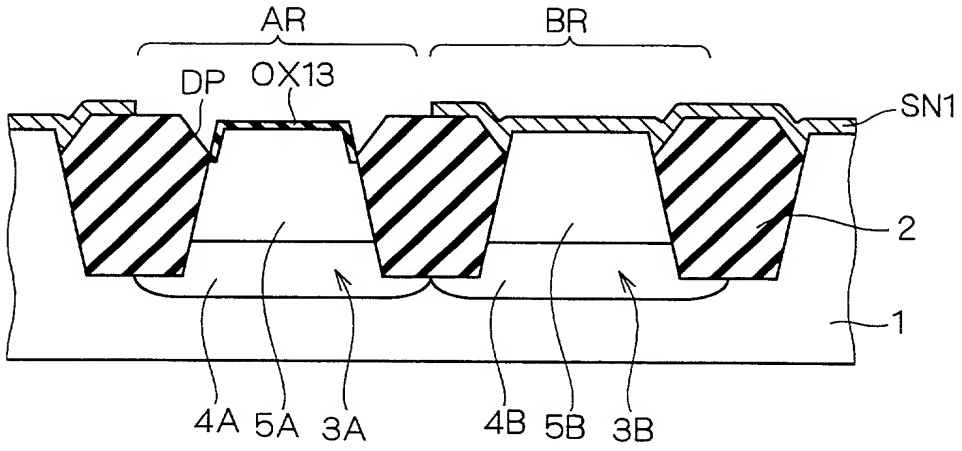
AR
BR
DP
OX13
SN1
2
1
4A
5A
3A
4B
5B
3B

FIG. 26

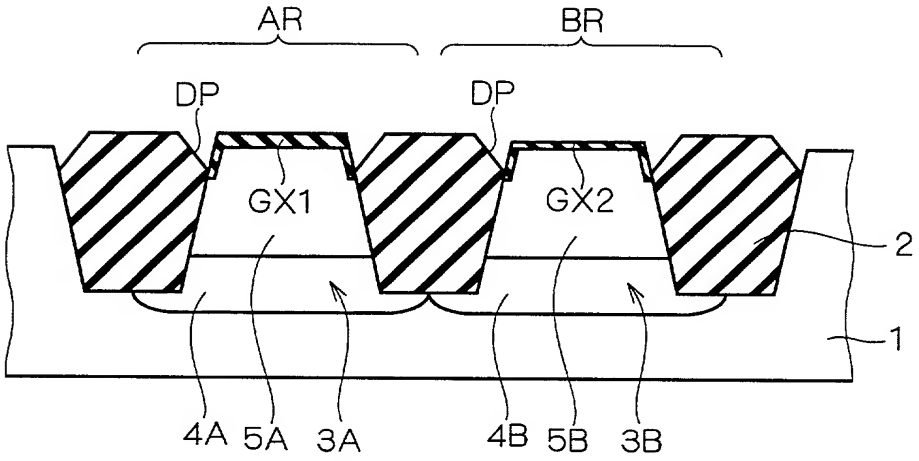
AR
BR
DP
DP
GX1
GX2
2
1
4A
5A
3A
4B
5B
3B

FIG. 27

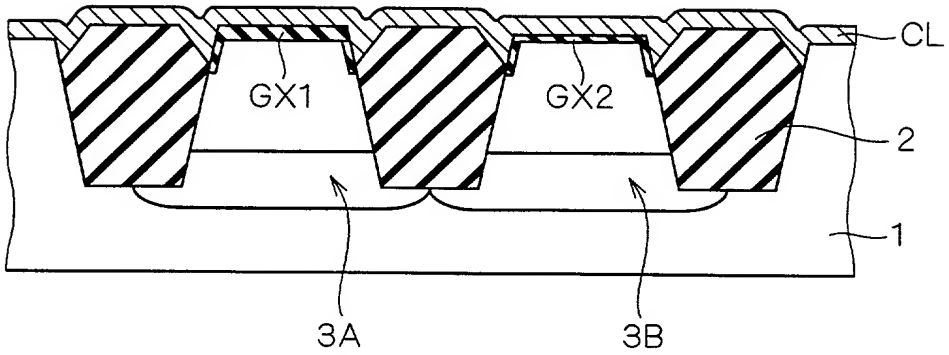

FIG. 28 (BACKGROUND ART)

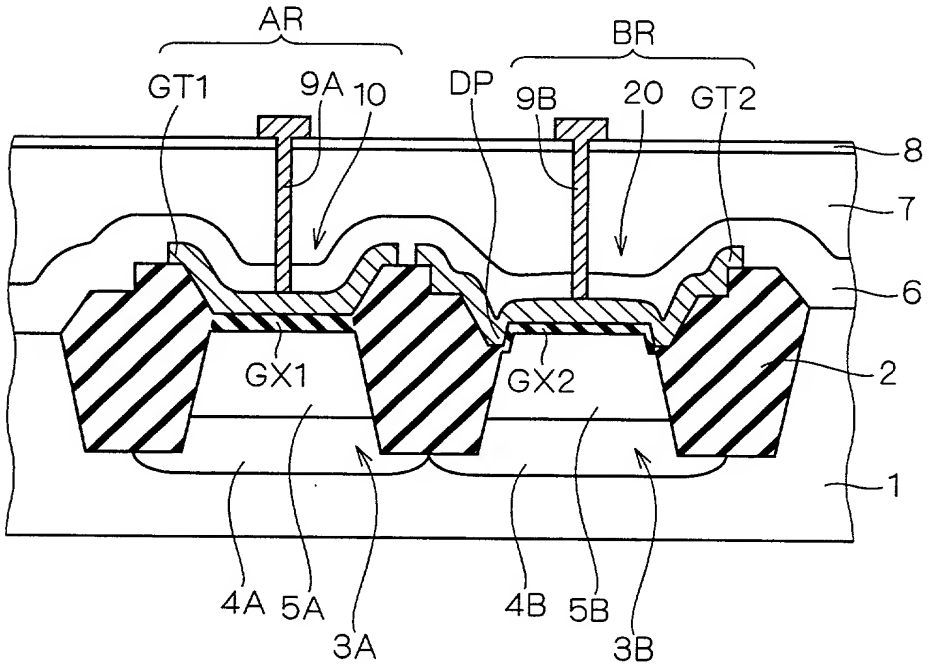

FIG. 29 (BACKGROUND ART)

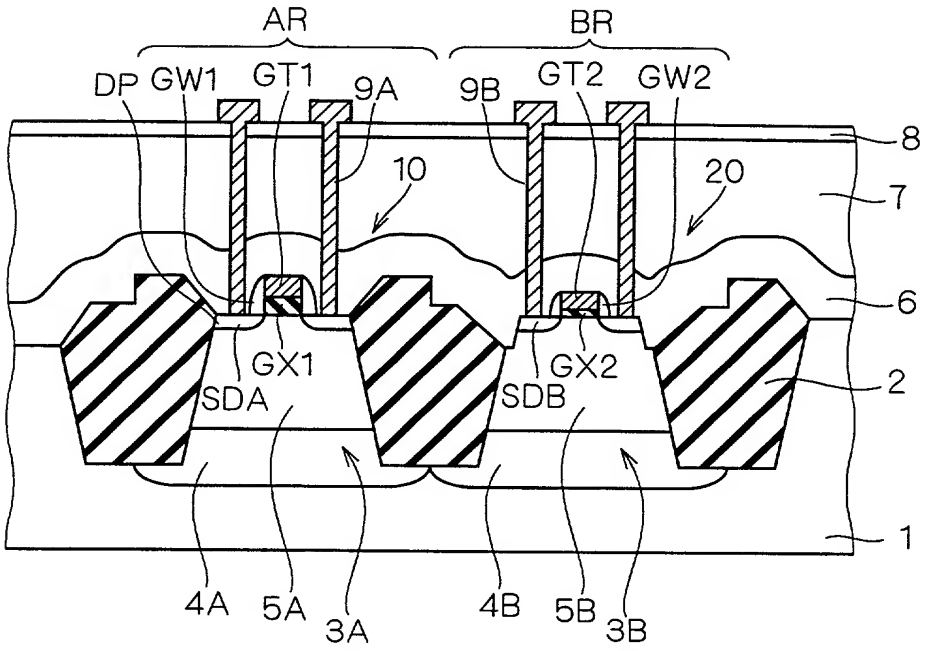

FIG. 30 (BACKGROUND ART)

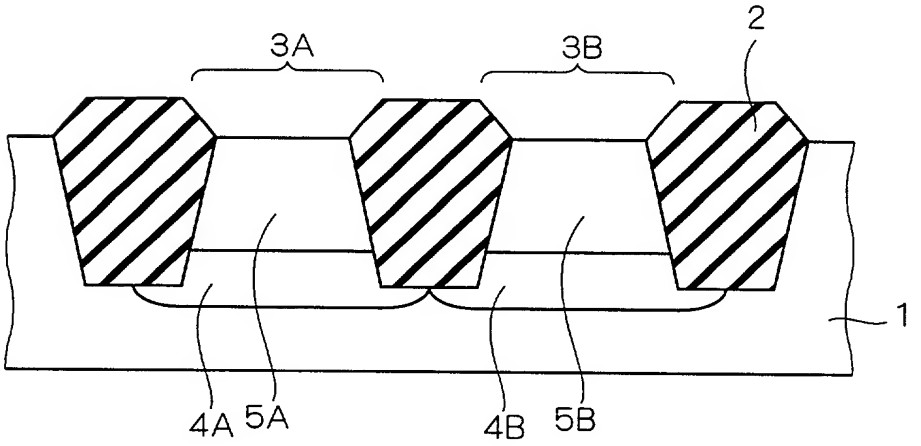

FIG. 31 (BACKGROUND ART)

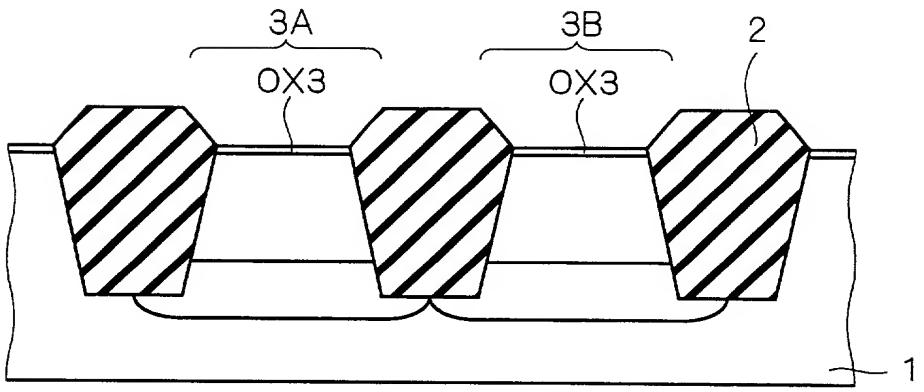

FIG. 32 (BACKGROUND ART)

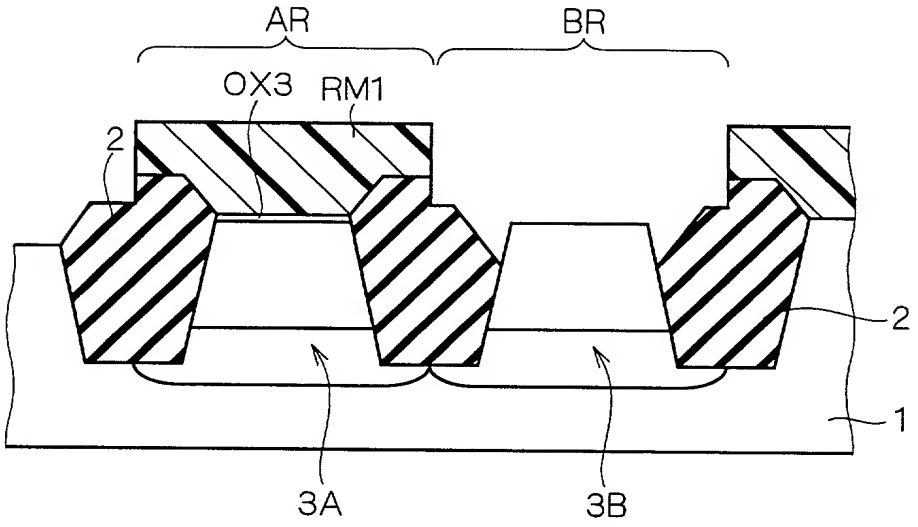

FIG. 33 (BACKGROUND ART)

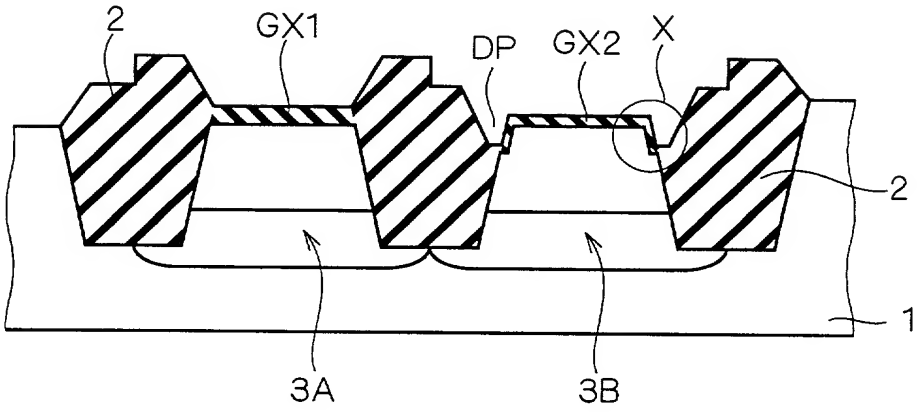

FIG. 34 (BACKGROUND ART)

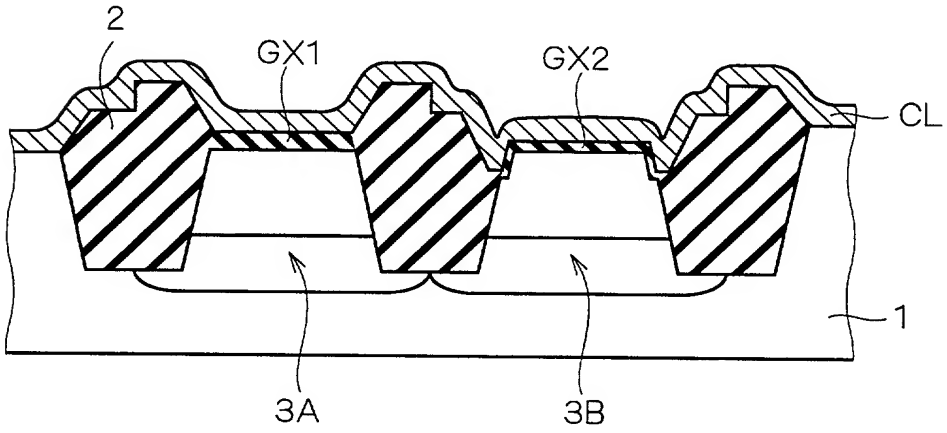

FIG. 35 (BACKGROUND ART)